

# DDR2 SDRAM

**SAE128M8 – 16 Meg x 8 x 8 banks**

**SAE64M8 – 16 Meg x 8 x 4 banks**

The SAE128M8U48T and SAE64M8U48T die were designed by Qimonda on the Qimonda 70nm trench process. The silicon is being run at Inotera, with EEMs testing and packaging the die. Refer to the Qimonda data sheet for exact timing and functionality.

**Table 1: EEMs: 8 x 12.5 Package**

SpecTek Part Number	Package	Qimonda Part Number
SAE128M8U48T08BBF-25	60 FBGA	HYB18T1G800C2F-2.5
SAE64M8U48T08BBF-25		HYB18T512800B2F-2.5

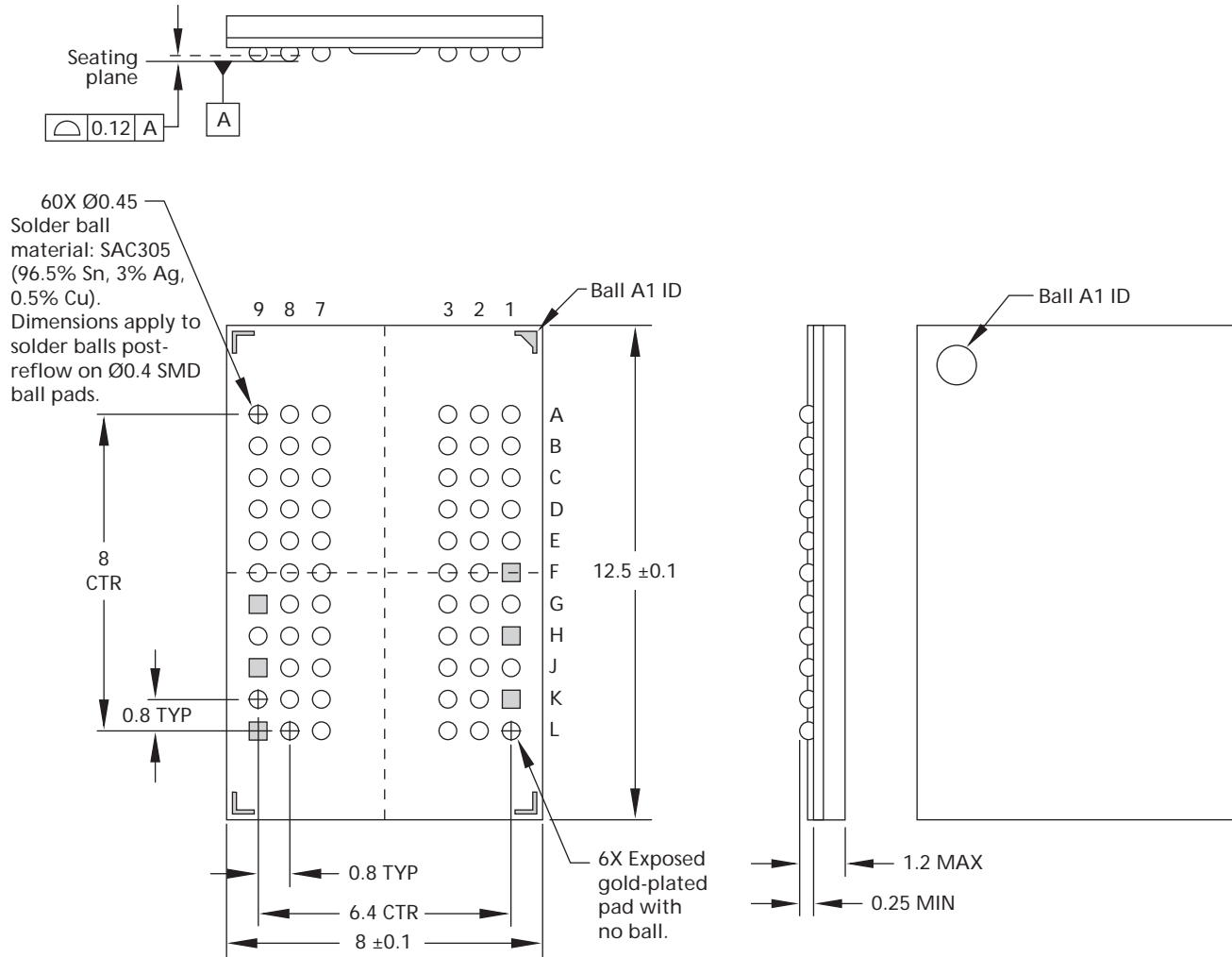
**Table 2: EEMs: 9 x 13 Package**

SpecTek Part Number	Package	Qimonda Part Number
SAE128M8U48T08BAF-25	60 FBGA	HYB18T1G800C2F-2.5
SAE64M8U48T08BAF-25		HYB18T512800B2F-2.5

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## Package Dimensions

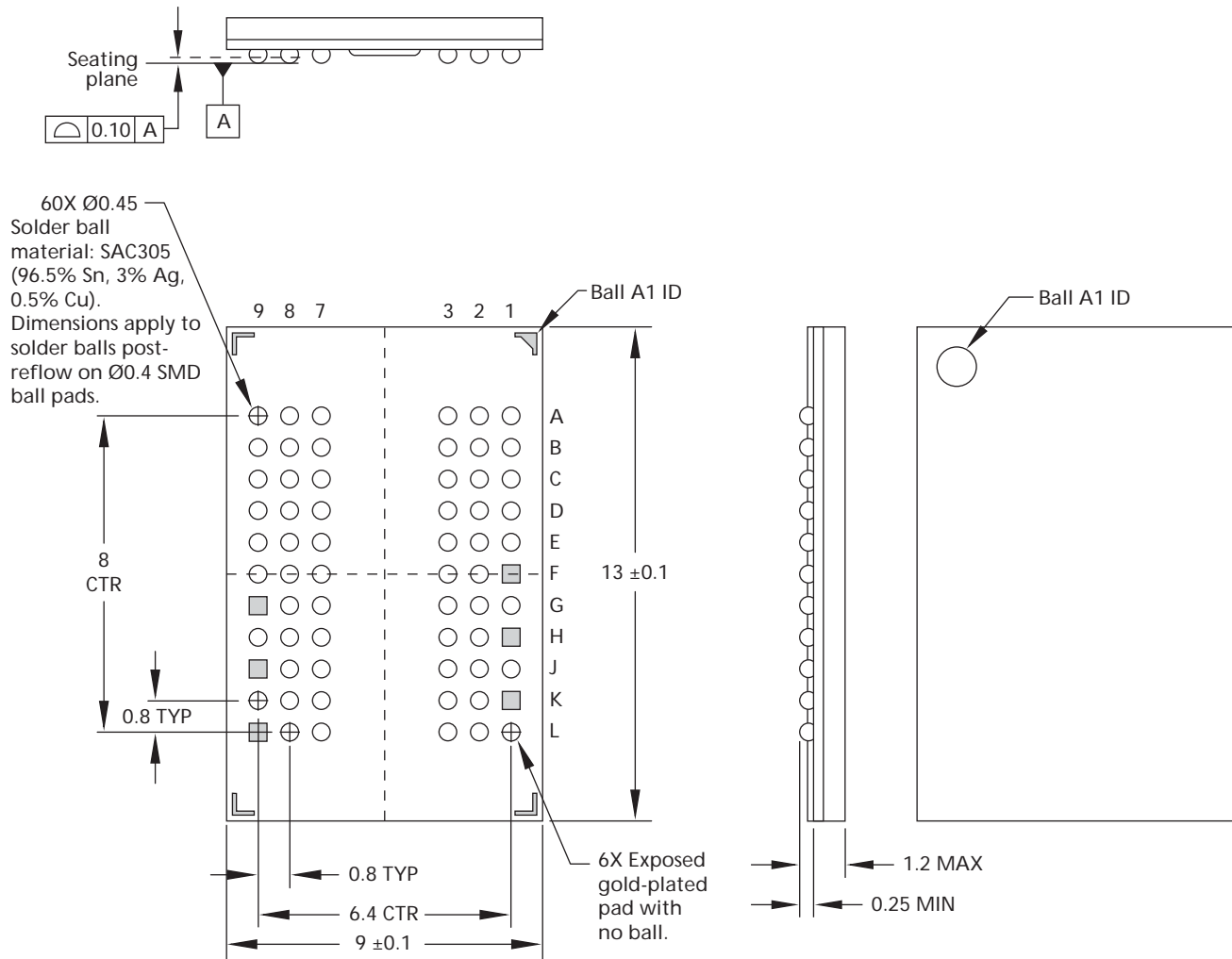
Figure 1: 8 x 12.5 Package



**Note:** All dimensions are in millimeters.

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Figure 2: 9 x 13 Package



**Note:** All dimensions are in millimeters.

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