

SpecTek Components Part Numbering System



Last Updated: 03/12/12

PRN 256M8 V79D G 9 GQ F - 15E

SpecTek/Customer Mark

SpecTek Component

- PRA = SpecTek Mark Assembly, 8 chip only
- PRN = SpecTek Mark, 8 chip only
- PRM = Logo Blast Mark, 8 chip only
- SGG = SpecTek Mark, component sale
- SMG = Logo Blast Mark; component sale
- SNL = SpecTek No Logo Mark

Settle & Ship

- XAA = SpecTek Generic (Generic Mark)
- XBA = SpecTek Generic (3 line Mark)
- XCB = Chip on Board (3 line Mark)

[Link to: DRAM Component Mark Reference](#)

[Link to: DRAM FBGA Mark Decoder](#)

[Link to: SS \(Sort and Settle\) Speed Descriptions and Values](#)

Speed/Speed Grade Field

SDRAM (JEDEC)

-75A (PC 133 3-3-3)

DDR (JEDEC)

-6A (PC-2700/DDR 333 2.5-3-3)

-5B (PC-3200/DDR 400 3-3-3)

DDR2 (JEDEC)

-37E (PC2-4200/DDR2 533 4-4-4)

-3 (PC2-5300/DDR2 667 5-5-5)

-25 (PC2-6400/DDR2 800 6-6-6)

-25E (PC2-6400/DDR2 5-5-5)

DDR3 (JEDEC)

-187 (PC3-8500/DDR3 1066 8-8-8)

-15 (PC3-10600/DDR3 1333 10-10-10)

-15E (PC3-10600/DDR3 1333 9-9-9)

-125 (PC3-12800/DDR3 1600 11-11-11)

Mobile LPDRAM SDRAM

-6 (6ns CL3 166MHz)

-5 (5ns CL3 200MHz)

L= PB

F= PB Free

Component Package

[Lead Count/Ball Array/
Package Size \(see next
page\)](#)

Refresh

2 = 2K

4 = 4K

8 = 8K Grade A ~98-100%

9 = 8K Grade B ~96-100%

A = 8K trimmed (blanket blown)

B = 8K 1.6V smart trimmed

C = 8K 1.4V smart trimmed

D = 8K Disabled Regulator

U = Undetermined

Component Depth and Width

Internal Designator

- Y= SDRAM
- T= DDR
- U= DDR2
- V =DDR3

Voltage*

- G = 1.5V DDR3
- J = 1.35V DDR3
- L= 3.3V SDRAM
- O= 1.8V DDR2
- V= 2.5V DDR
- U= Undetermined

