

SpecTek NOR MCP Part Numbering System



Last Update: 02/25/2013

N M JXM5 28M 64M A A CB C A A ABZ - 96BT

SpecTek Memory

M = Marked
U = Unmarked
P = Micron Partial Mark

Design ID

NOR Density

16M = 16Mb 56M = 256Mb
32M = 32Mb 12M = 512Mb
64M = 64Mb 24M = 1Gib
28M = 128Mb 48M = 2Gib

RAM Density

16M = 16Mb 12M = 512Mb
32M = 32Mb 24M = 1Gib
64M = 64Mb 48M = 2Gib
28M = 128Mb 96M = 4Gib
56M = 256Mb 92M = 8Gib

Operating Voltage Range

A = 1.7V – 1.95V
C = 1.5V – 1.7V
D = 1.2V – 1.5V
F = 1.0V – 1.2V

NAND Flash I/O Width

	Width	Design ID	Width	Design ID	
A	x16	QKH1	H	x16	QKKM
B	x16	QKHM	J	x16	QKLO
C	x16	QKJ1	K	x16	QKLM
D	x16	QKJL	L	x16	QLKL
E	x16	QKJM			
F	x16	QKK1			
G	x16	QKKL			

RAM I/O Configuration

BA = DRAM – T35M x16	CB = PSRAM – P25Z x16
BB = DRAM – T36N x16	CC = PSRAM – P26Z x16
BC = DRAM – T67M x16	CD = PSRAM – P27Z x16
CA = PSRAM – P24Z x16	CE = PSRAM – (2) P26Z x16
NA = Non-applicable	

Speed Grade

BT = B Grade Fully Tested at 70° (100% of NAND Density)
BU = Untested
96BT = 96ns B Grade Fully Tested at 70° (100% of NAND Density)
100BT = 100ns B Grade Fully Tested at 70° (100% of NAND Density)
BP = Bagged Product
96FT = 96ns B Grade Fully Tested at 90° (100% of NAND Density)
100FT = 100ns B Grade Fully Tested at 70° (100% of NAND Density)

Package Code

	Balls	Pkg	Outline	Pkg Height	Pkg Type	Pitch
15Z	104	MCP	10x10	1.10mm	TFBGA	0.65mm
19Z	64	MCP	7.7x9	1.00mm	VFBGA	0.50mm
1EZ	56	MCP	11x8	1.20mm	TFBGA	0.50mm
1UZ	56	MCP	8x8	1.20mm	TFBGA	0.50mm
1WZ	84	MCP	8x10	1.20mm	TFBGA	0.80mm
3NZ	128	MCP	12x12	0.892mm	VFBGA	0.65mm
3RZ	133	MCP	8x8	1.00mm	VFBGA	0.50mm
3TZ	107	MCP	8x11	1.20mm	TFBGA	0.80mm
45Z	88	MCP	8x10	1.20mm	TFBGA	0.80mm
4EZ	56	MCP	8x8	1.00mm	VFBGA	0.50mm
4WZ	56	MCP	7.7x6.2	1.00mm	VFBGA	0.50mm
51Z	133	MCP	8x8	1.00mm	VFBGA	0.50mm
5CZ	84	MCP	8x10	1.20mm	TFBGA	
63Z	165	MCP	9x11	1.20mm	TFBGA	
7BZ	84	MCP	8x10	1.20mm	TFBGA	
8TZ	107	MCP	11x11	1.20mm	TFBGA	0.80mm
AAZ	56	MCP	8x8	1.00mm	VFBGA	0.50mm
ABZ	56	MCP	7.7x6.2	1.00mm	VFBGA	0.50mm
C8Z	128	MCP	12x12	1.10mm	TFBGA	0.65mm
KWZ	104	MCP	10x10	0.90mm	VFBGA	0.65mm
QKZ	107	MCP	8x10	1.20mm	TFBGA	0.80mm
X1Z	44	MCP	7.7x6.2	1.00mm	VFBGA	0.50mm
XGZ	128	MCP	12x12	1.15mm	TFBGA	0.65mm

Chip Count

A = 1 NOR, 1 RAM E = 1 NOR, 3 RAM
B = 2 NOR, 1 RAM F = 2 NOR, 3 RAM
C = 1 NOR, 2 RAM G = 1 NOR, 4 RAM
D = 2 NOR, 2 RAM X = Undetermined

Multiplex Interface

A = ADMUX D = SPI
B = NONMUX X = Undetermined
C = QAADMUX

Boot Block

A = TOP
B = BOTTOM
C = NO BOOT
X = Undetermined